In re Patent Application of   :   Docket No.: YOR920030401US1

   Chandramouli Visweswariah   :   Group Art Unit:

Serial No.:   To be Assigned   :   Examiner:

Filed: Herewith   :   Date: September 18, 2003

For: **SYSTEM AND METHOD FOR INCREMENTAL STATISTICAL TIMING ANALYSIS OF DIGITAL CIRCUITS**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
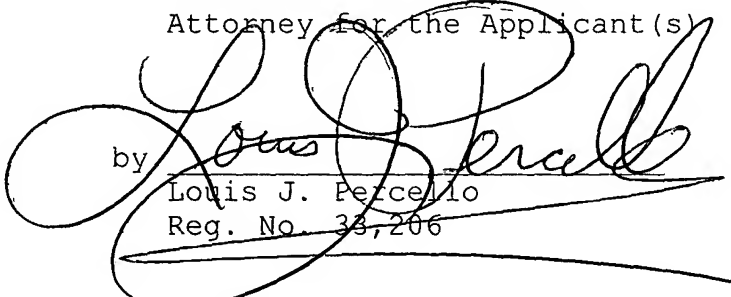Box Patent Application
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the document listed on the accompanying form PTO-1449. A copy of the listed document is enclosed. It is respectfully requested that the Examiner consider the cited document and return an initialed copy of the form PTO-1449.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,
Attorney for the Applicant(s)

by Louis J. Percello
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FORM PTO-1449 (Modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. : YOR920030401US1	SERIAL NO.: CONFIRMATION NO.
	APPLICANT: Chandramouli Visweswariah	
(Use several sheets if necessary)	FILING DATE:	GROUP:

REFERENCE DESIGNATION   **U.S. PATENT DOCUMENTS**

EXAMINER INITIALS		DOCUMENT NUMBER						DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
	AA	5	5	0	8	9	3 7	4/10/1996	Abato, et al	364/488	GO6F 17/50	5/16/1993
	AB											
	AC											
	AD											
	AE											
	AF											
	AG											
	AH											
	AI											

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER						DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
												YES	NO
	AJ												

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

	AK	"Explicit Computation of Performance as a Function of Process Variation", L. Scheffer, TAU'02,December 2-3. 2002, Monterey, California, USA
	AL	"Timing Yield Estimation from Static Timing Analysis", Anne Gattiker, Sani Nassif, Rashmi Dinakar, Chris Long. IEEE International Symposium on Quality Electronic Design, 2001
	AM	"Fast Statistical Timing Analysis by Probabilistic Event Propagation", Jing-Jia Liou, Kwang-Ting Cheng, Sandip Kundu, Angela Krstic, DAC 2001, June 18-22, 2001, Las Vegas, Nevada, USA
	AN	"Statistical Timing for Parametric Yield Prediction of Digital Integrated Circuits" J.A. G. Jess, K. Kalafala, S.R. Naidu, R.H.J.M. Otten, C. Visweswariah, DAC 2003, June 2-6, 2003, Anaheim, California, USA

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.